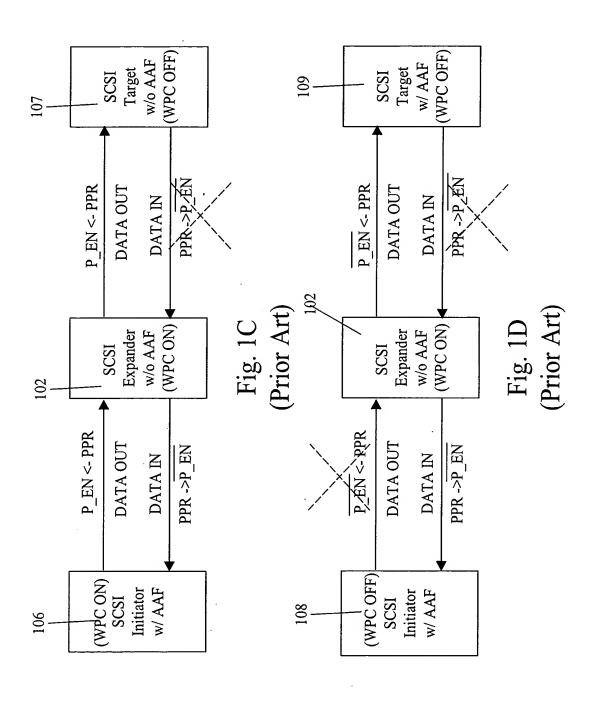
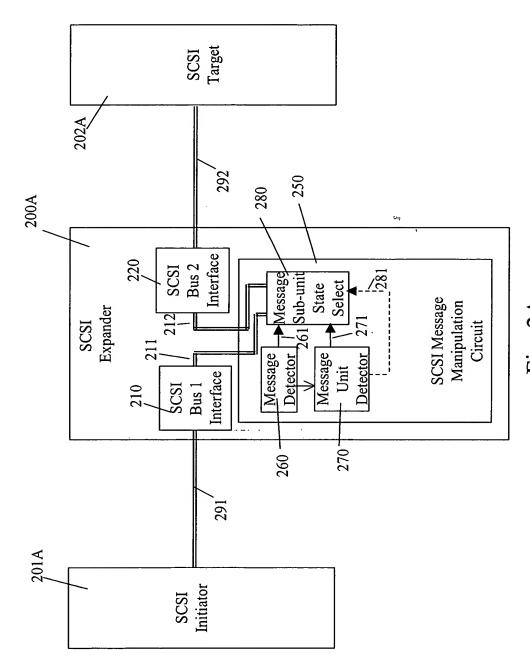


Fig. 1B (Prior Art)

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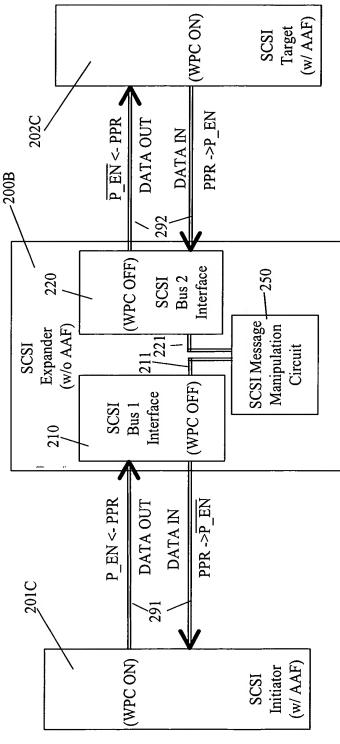
Fig. 2A

Fig. 2B

Fig. 2C

Fig. 2D

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Fig. 2E

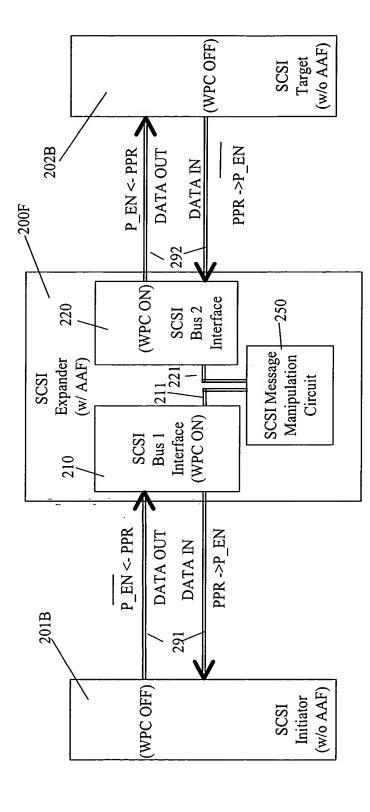


Fig. 2F

Fig. 2G

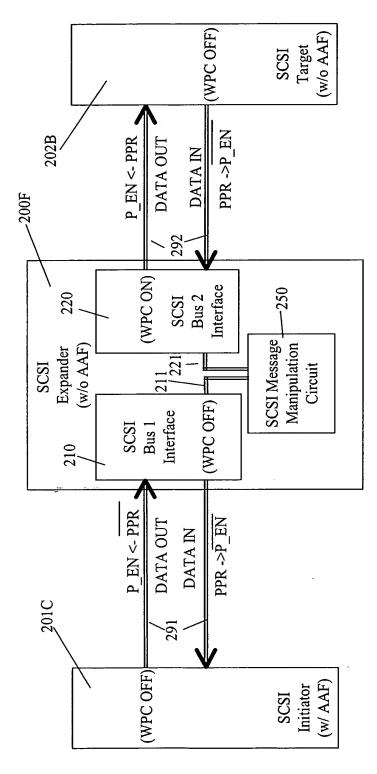


Fig. 2H

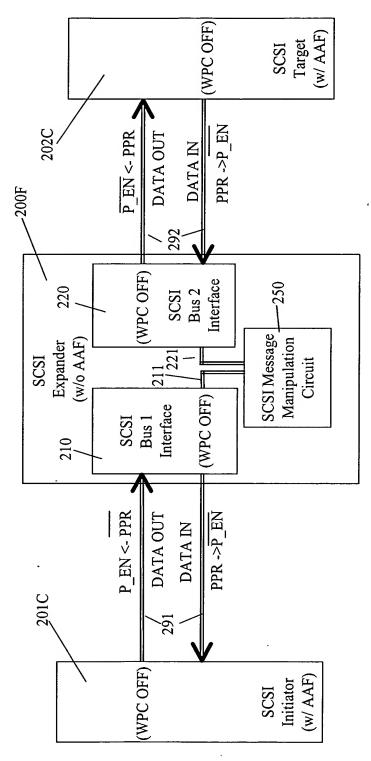


Fig. 2I

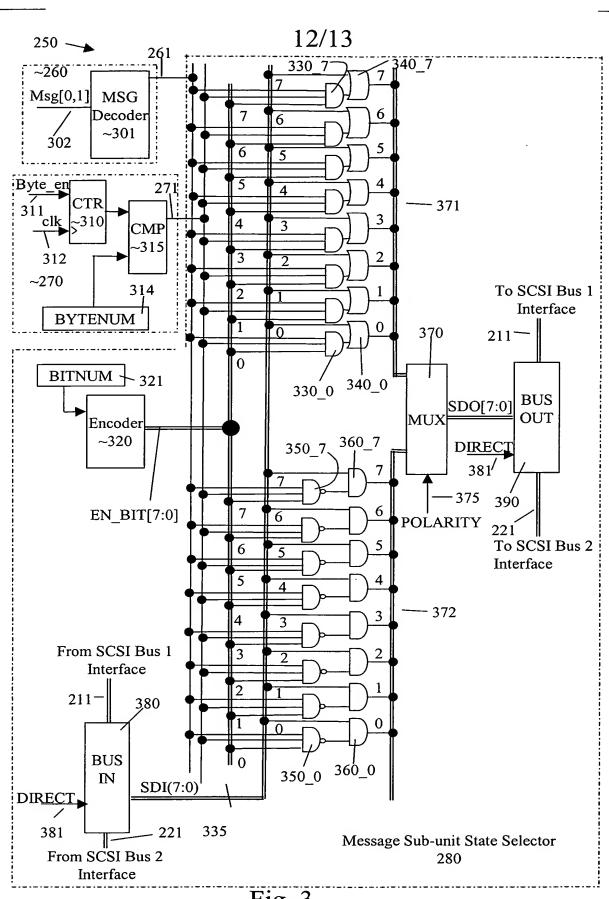
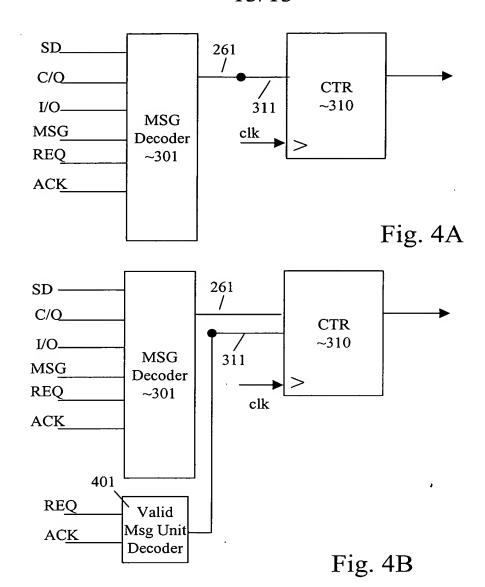


Fig. 3





ACK

SD Valid Valid T -> I I/O = 0

SD Valid Valid I -> T I/O = 1

Fig. 4C